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To: UVLBI Group From: A. Burns and J. SooHoo Subject: Downconverter for LMT Mexico

Attached in Figure 1 is the circuit diagram for the Downconverter as designed by Alan E.E. Rogers. To build the downconverter a 1U chassis was used and the components placed according to fitment and to reach the outputs on the back panel of the chassis. The components were attached to the chassis using #2 hardware and 12VDC power was supplied via a terminal breakout strip located next to the power supply. Figure 2 shows the item listing used in assembly and specifications are linked where possible. Figures 3 and 4 are photographs depicting the rear panel and circuit, respectively. Rotary step attenuators on the front panel allow for adjustment in both channels without changing internal components. Initial testing consists of verifying power and ground to amplifiers, and signal is present out of all four outputs. We also injected a test signal with broadband noise through to verify with a spectrum analyzer that we were able to see the signal mixed with the second LO in both the low and high.

The downconverter was shipped to INAOE and brought up to the LMT site. We ran through the same test signal tests on site for additional verification (Figure 5). Also we had redone some measurements regarding the power levels with the following results:

- With nothing connected to the spectrum analyzer = -76dBm 1MHz RBW
- Connecting the downconverter with no receiver connected, attenuator knob set to z0 = -56dBm 1MHz RBW
- Receiver connected to downconverter and attenuator know set to 0 = -37dBm 1MHz RBW



Figure 1. Circuit Diagram by A.E.E.R.



Figure 2. Items listing and spec sheets (When available)



Figure 3. Rear Panel showing EMI filter and feed throughs



Figure 4. Assembled Circuit with top panel removed.



Figure 5. Test signal injection test.