DBE Site Configuration
March 2008

Steven Durand
presenting
for the
Electronics Division
VBDE Block Diagram
Version 6
VLBA Station
Configuration Version 6
March 2008 Configuration
IBOB 2
IBOB2 Layout
By Mike Revnell
VLBA Station Configuration Version 5

Timing module

VDBE (1)

VDBE (1)

VDBE (1)

Ethernet switch

1GE (4) M&C

T-1 Network connection

1GE (4) Data

MARK5 A

MARK5 C

10GE Data
VBDE Clock Board

Designed by MIT Haystack
Electronics Division

Manufacturing Goals

IF Distributor

IF Distributor

Timing Module

Ethernet Switch

DBE (1-4)

Maser Room

Computer Room

Mark5 C
Casper Collaborators

- Radio Astronomy Lab (RAL)
- Berkeley Wireless Research Center (BWRC)
- Space Sciences Laboratory (SSL)
- MIT Haystack Observatory
- Allen Telescope Array (ATA)
- Combined Array for Research in Millimeter-wave Astronomy (CARMA)
- Jet Propulsion Laboratory (JPL)
- National Radio Astronomy Observatory (NRAO)
- National Astronomy and Ionosphere Center (NAIC)
- Square Kilometer Array (SKA)
- South Africa SKA
- Commonwealth Scientific and Industrial Research Organization (CSIRO)
- Long Wavelength Array (LWA)
- Sub millimeter Array (SMA)
- Jodrell Bank
- University of Arizona and Caltech
## AT-9448Ts/XP

48-Port 10/100/1000T Managed Layer 3 Switch with 2 10GbE XFP Bays and a Stacking Module Expansion Bay with 48Gbps Stack Backplane

### AT-9448Ts/XP-xx

Layer 3 stackable switch with 48-port 10/100/1000Base-T plus 2 XFPs bays (unpopulated) and memory flash card slot

### Network QoS and IGMP for Video and Voice over IP

A rich offering of voice and video networking features is incorporated to ensure support for demanding multimedia networking applications

### Key Features

- **10 Gigabit Ethernet Support**
- Two integrated XFP bays for 10GbE transceiver modules