



RADIOBLOCKS goals

- Building blocks suitable for multiple facilities
- Joint effort to solve common problems
- Enabling new scientific discoveries in mid- and long-term
 - Sensitivity

BLOCKS

- Bandwidth
- Field-of-view

Address the whole signal chain from analog to post-processing

Keeping EU at the front in radio technology developments



The RADIOBLOCKS project contribution to the VLBI of the future

Marjolein Verkouter (JIVE) for:

de Vicente P., Yebes Observatory (IGN), Kramer C., Institute de Radioastronomie Millimetrique,

Wieching G., Max Planck Institut fuer Radioastronomie, Romein J., ASTRON, Bemmel I. v, ASTRON,

Beswick R., University of Manchester, Verkouter M., JIVE, Attema-van Waas R., ASTRON, Słowikowska A., JIVE, Cimo G., JIVE



RADIOBLOCKS partners



First time that **companies** participate in these type of projects. Required by the EU



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Project in a nutshell

EU INFRA-TECH (*Horizon Europe*) March 2023 to March 2027

24 Beneficiaries of the EU grant: 9M€ 8 Associate non-EU partners: 3M€

32 partners from 12 countries (incl. ESO and SKAO)

1187 person months

3

4

BLOCKS



Project in a nutshell

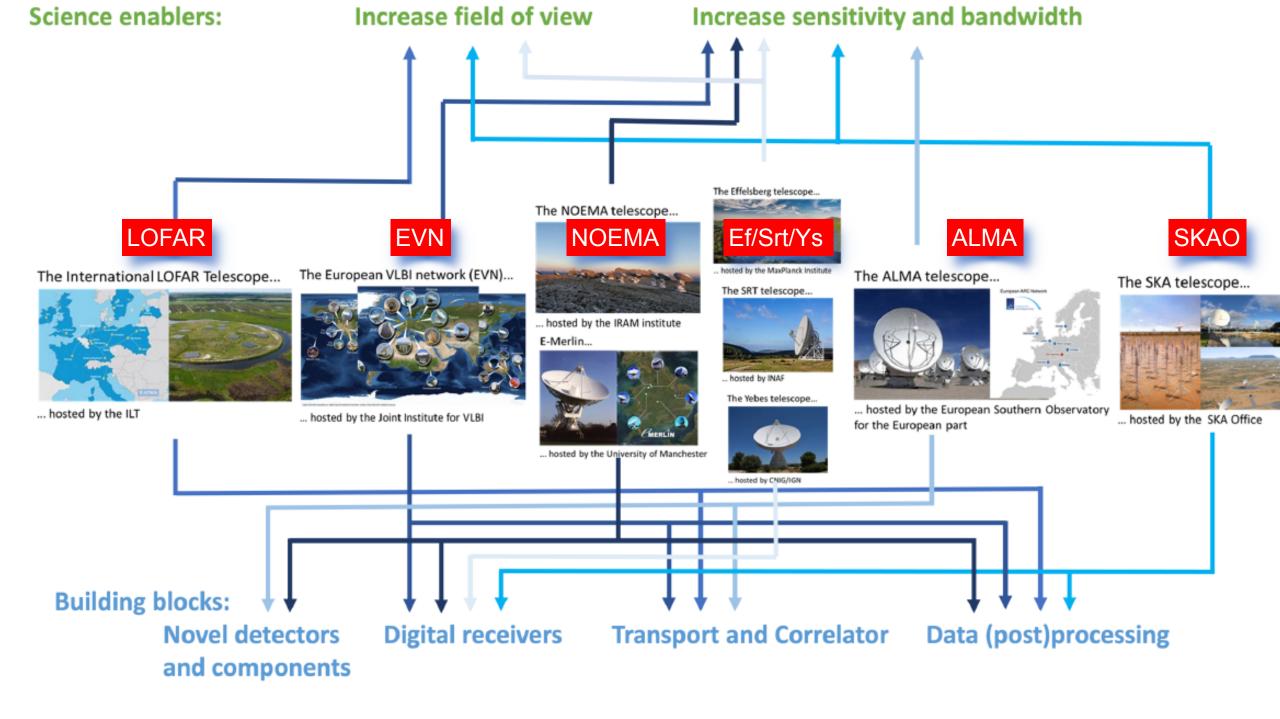
Project management

Novel detectors and components

Digital receivers

Data transport and correlation

Data processing

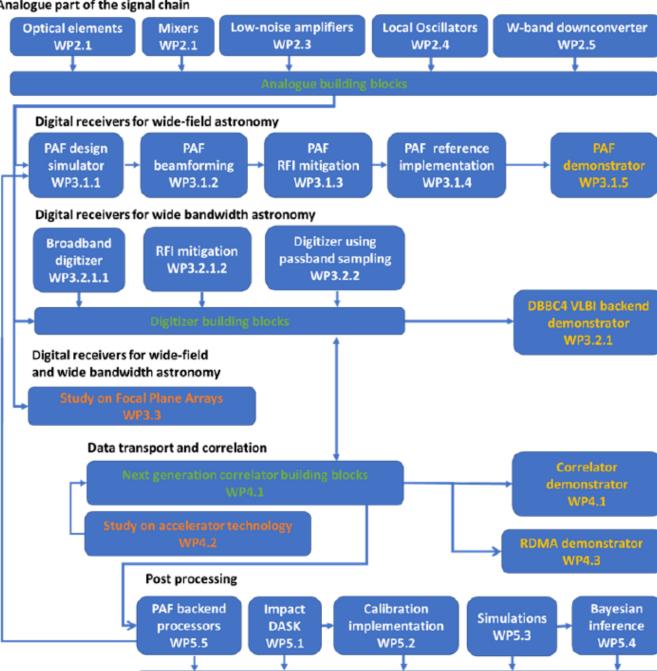




RADIOBLOCKS for the VLBI of the future

Not *specifically* designed for VLBI: but for single dish, connected interferometry and VLBI.

RFI: resilience & mitigation	WP2, 3
 Larger bandwidth 	WP2, 4, 5
Sensitivity	WP2, 4, 5
 Wide field-of-view 	WP4
• FP Arrays (field of view & high prec. astro	ometry) WP2, 3, 5
• Early digitization (close to the frontend)	WP3



Analogue part of the signal chain

Focal Plane Arrays require:

- Compactness (= better integration of OMTs, mixers, LNAs)
- Easier and faster manufacturing
- DC power (less dissipation = lower load for cryogenic cooler)
- uW power (higher LO power needed to feed all pixels)

Larger bandwidth & larger arrays

• Generate larger volumes of data

Digitization starting closer to frontend

- Phased Array Feeds.
- Data filtering & no frequency dependent power loss for downconverted signals



WP1 Project management

"Effective, transparent and pro-active management which guarantees the smooth implementation of the financial, administrative and technical activities into the project"

Coordinator: Aga Słowikowska (JIVE) Management Team: Giuseppe Cimo' (PM), Arpad Szomoru, Aukelien van den Poll

3 periodic reports (months 12, 30 & 48)21 milestone reports23 deliverables



WP2 Novel detectors & components

"Developing key components for future sensitive, wideband receivers for European research infrastructures"

Analogue parts of the signal chain

Coordinated by Carsten Kramer (IRAM)

16 institutes & *companies*:

IRAM, Fraunhofer – IAF, ESO, GARD, UOXF, MPG, RUG, UKRI, TUD, UNIMAN, CNIG, UCO, INAF, OBSPARIS, HES-SO, *Lytid, TTI-Norte, RUAG*



WP2 Tasks

- (Task 2.1.1) RF windows, lenses, filters
 - Development of wideband nearly reflectionless flat lenses

•(Task 2.1.2) Orthomode-Transducers

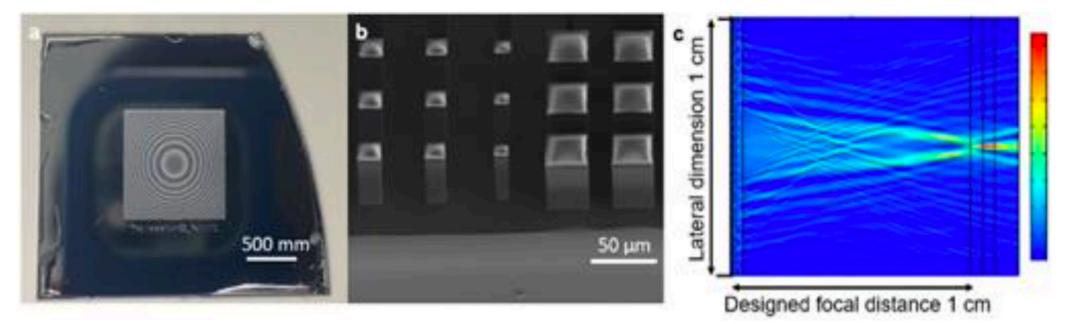
- Waveguide and planar technology from 100 GHz to 650 GHz
- Planar technology to reduce size (improved integration)
- (Task 2.1.3) Horns
 - High performance corrugated horns: difficult to mass produce
 - Silicon micromachining: new technology for easier manufacturing

GHZ

600







Metalens (TUD)

Si nanopillars (at 30deg)

Simulation of finite element focussing effect



WP2 Tasks

• (Task 2.2) SIS mixers

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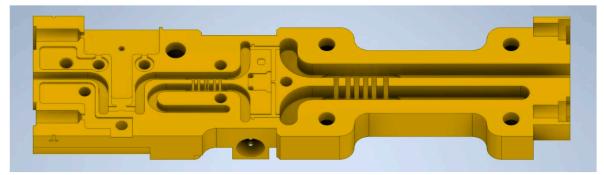
- Expanding RF and IF bandwidth
- Exploring new materials (AIN...)

- (Task 2.3) LNAs:
 - RF LNAs: 67 150 GHz integrated IF mixers
 - IF LNAs:
 - Bandwidth expansion up to 30 GHz
 - Different technologies to improve sensitivity (mHEMT MMICs, Inp HEMT, superconductive parametric amplifiers)

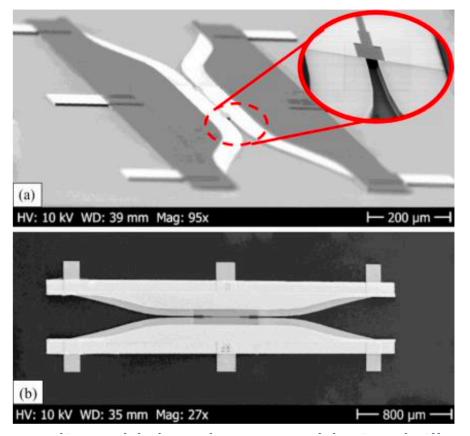
Sensilivity	Bandwidth	Focal Plane Arrays		
T MMICs				







RF LNA: CAD design of integration of LNA and SHIRM using a W-band hybrid coupler



Ultra-wide band waveguide to slotline transition



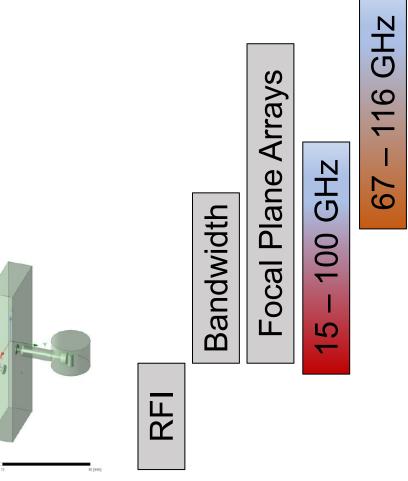


WP2 Tasks

- (Task 2.4) Local Oscillators:
 - Up to 345 GHz

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- Increasing power and efficiency
- (Task 2.5) W-Band downconverter modules
 - MMIC+LOs (67 116 GHz)
- (Task 2.6) Tunable filters
 - Before the LNAs to reject RFI







WP3 Digital receivers

"Deployment of state-of-the art digital technology to improve system temperature, bandwidth and field-of-view"

Coordinated by Gundolf Wieching (MPG)

6 institutes & *companies*:

MPG, RUG, UNIMAN, INAF, HES-SO, RUAG



WP3 Task 3.1 (wide field)

- (Task 3.1.1) PAF Design Simulator Wide-field astronomy
 - Simulate the effect of RFI-sources & instrumental misbehaviour
 - Already available in MPIfR Github
- •(Task 3.1.2) Novel Beamforming technology on RFSoC
 - Characterize the RFSoC (FPGA) performance for PAF systems
 and design and build a prototype beamformer
- (Task 3.1.3) PAF-RFI mitigation and adaptive beamforming methods
 - Adaptive filtering, generalised adaptive beamforming strategies and software for use on arbitrary phased array feeds



WP3 Task 3.1 (wide field)

- (Task 3.1.4) PAF Reference implementation
 - Simulate the sky as seen by a target PAF design and use it as a digital signal processing reference.

•(Task 3.1.5) Demonstrator

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Provide the 100m Effelsberg CryoPAF (conveniently modified) as a demonstrator platform for tasks 3.1.2, 3.1.3 and 5.5



WP3 Task 3.2 (wide BW)

- (Task 3.2.1) DBBC4 VLBI backend demonstrator
 - Hardware developments for a high performance VLBI backend prototype
 - BW: 256 GHz, output 1 Tb/s (2 bits)
- (Task 3.2.2) DiFrEnd28 Broadband digitizer
 - Direct digitization and formatting of 2 analogue 28 GHz BW inputs.
- (Task 3.2.3) RFI-Mitigation: A-EYE Artificial Intelligence Controller
 AI techniques for allowing near-realtime recognition and mitigation (Multi CPU FPGA)

E C



WP3 Task 3.2 (wide BW)

- •(Task 3.2.4) Digitizer using passband sampling
 - Alternative technology to wide-bandwidth digitizer (10 bits)
 - Applicable to SKA band 6: 14-25 GHz, expandable to 40 GHz.
 - Potential VLBI in 35-40 and 35-50 GHz bands

- (Task 3.3) Multi-pixel astronomy via Focal Plane Arrays
 - Wide-field wide-band radio astronomy at high-frequencies
 - Multiple beams on the sky via mechanical components



lane

oca



WP4 Data transport and correlation

"to deliver a collection of efficient, and/or high-performance signal processing building blocks exploiting commercially available hardware accelerator platforms"

John Romein & Ilse v. Bemmel (ASTRON)

9 institutes & *companies*: ASTRON, JIV-ERIC, ESO, VENTSPILS, UBx, *Sioux*, KASI, NAOJ, UNIMAN





Objectives and goals

Boost efficiency by exploiting commercially available hardware

This will lead to increased science potentia

- Higher sensitivity (more bandwidth)
- Larger field-of-view

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Sustainable systems

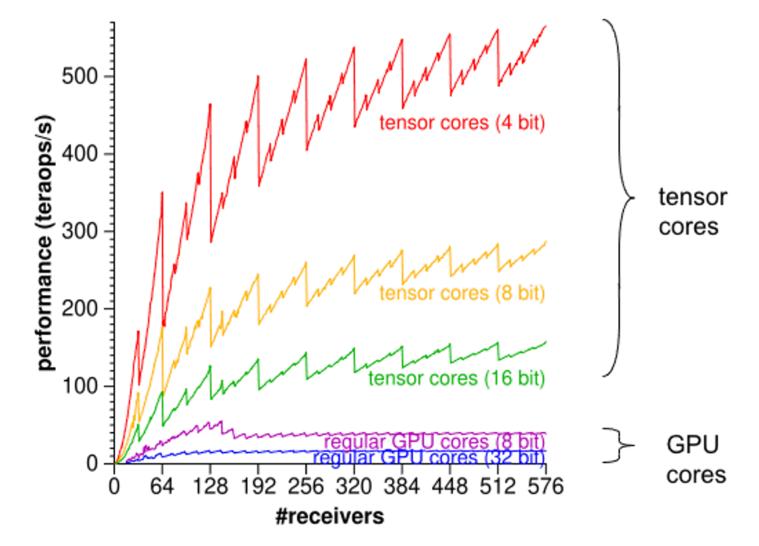
Of interest for e.g. ALMA, EVN, LOFAR2

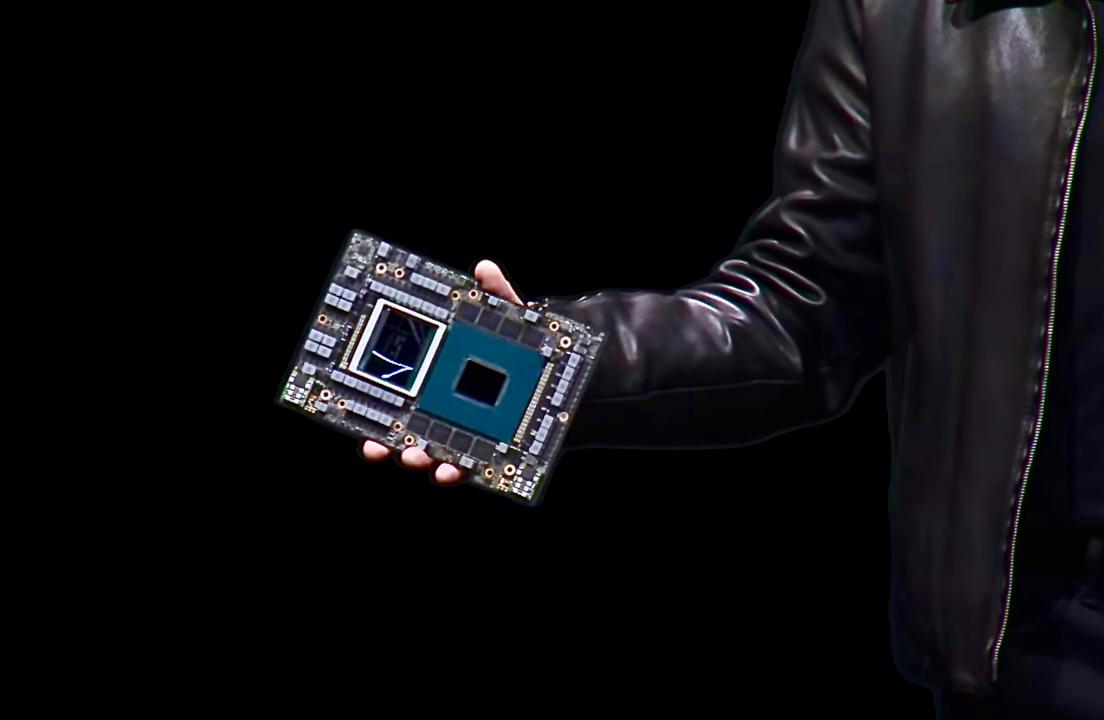


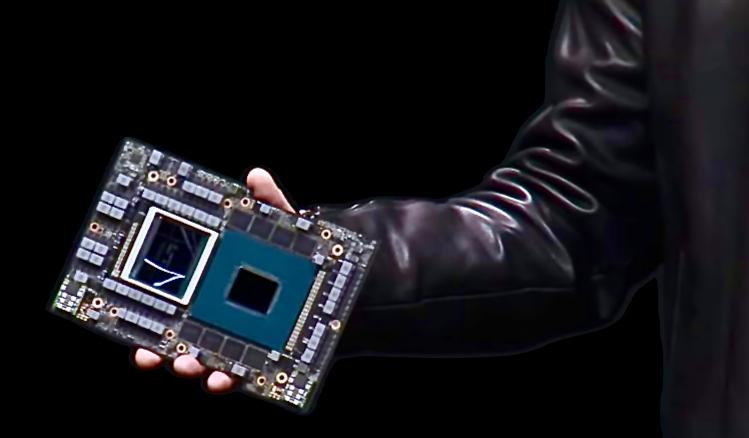


- 1. Next generation correlators building blocks: TensorCore
- 2. Accelerator research report: exploring alternative HPC tech
- 3. High-speed data transport development: moving to >100Gb/s

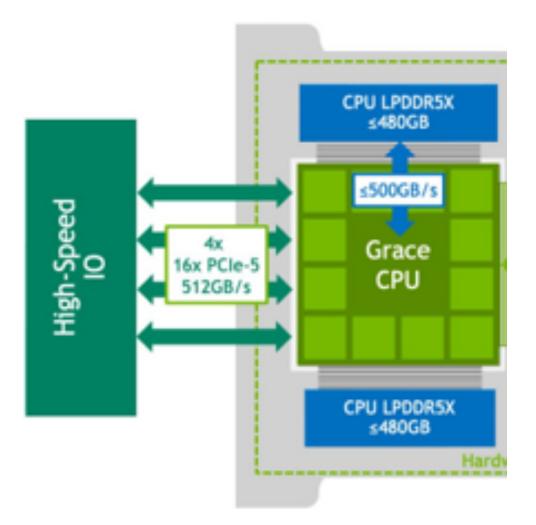
Current status: TensorCore correlator

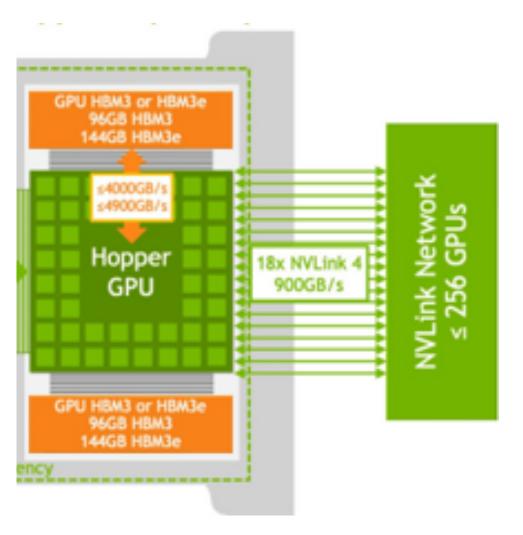




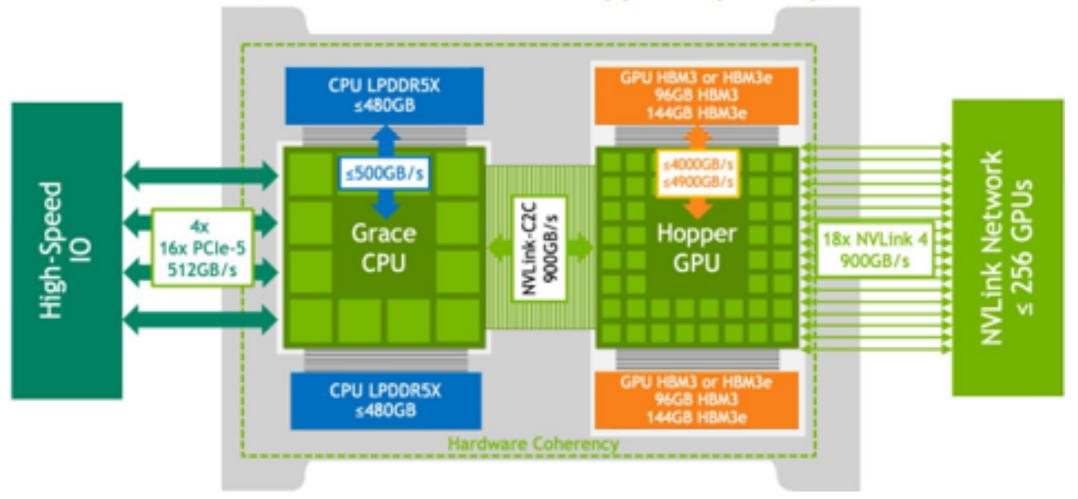


the NVIDIA© Grace Hopper™ SuperChip

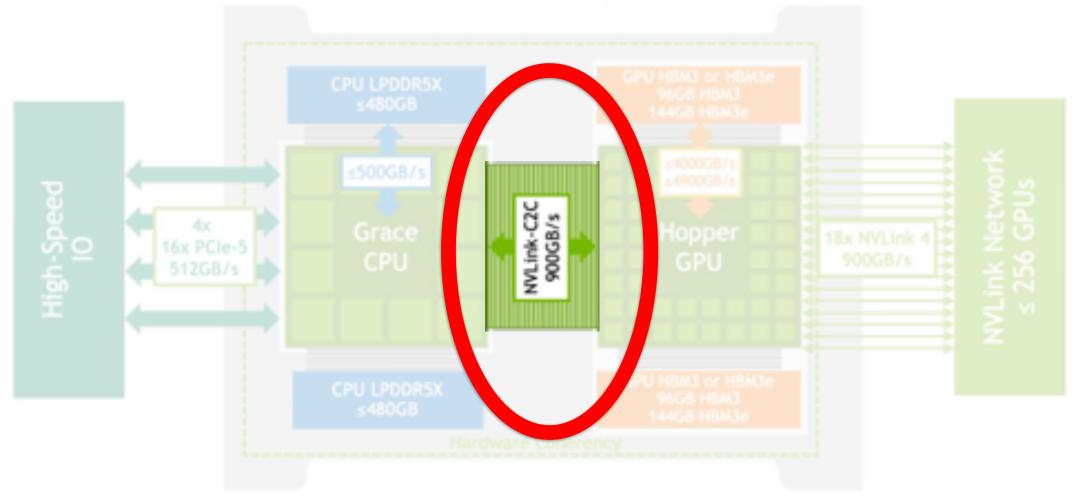




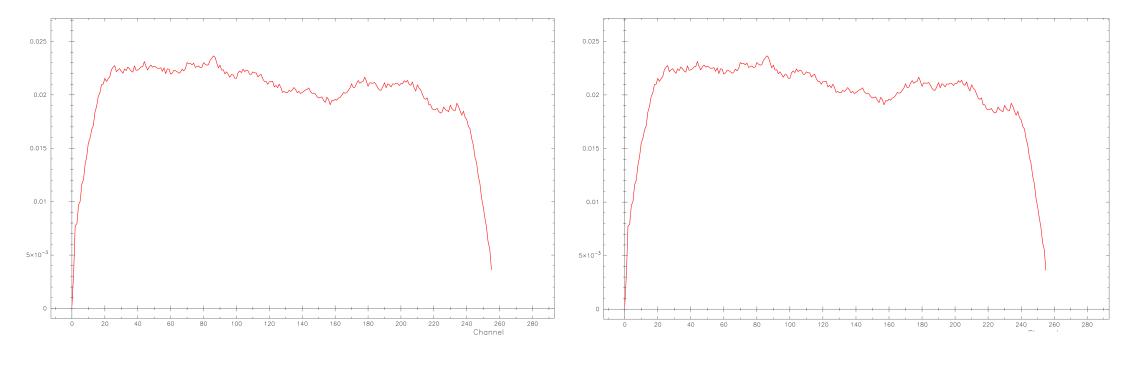
NVIDIA GH200 Grace Hopper Superchip



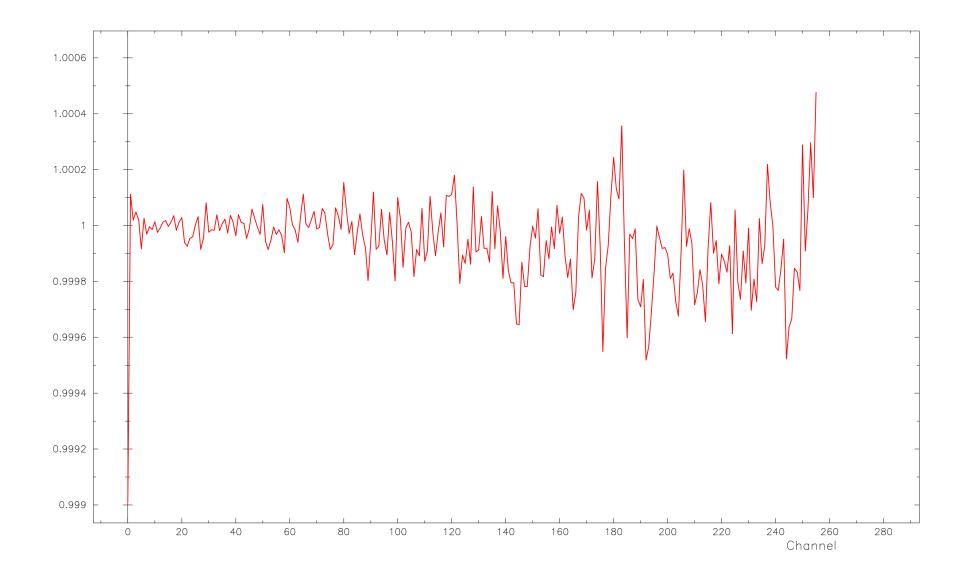
NVIDIA GH200 Grace Hopper Superchip



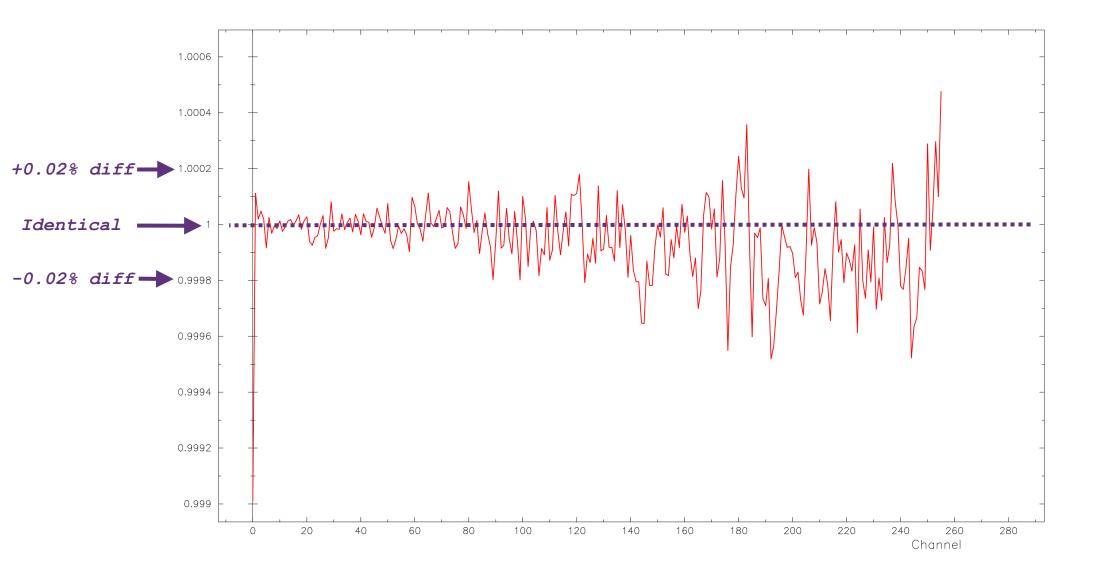
Effelsberg-Onsala baseline cross-correlation bandpass (N22L3)



SFXC (CPU) SFXC (GPU)



GPU/CPU bandpass ratio - a small scaling issue (under investigation)



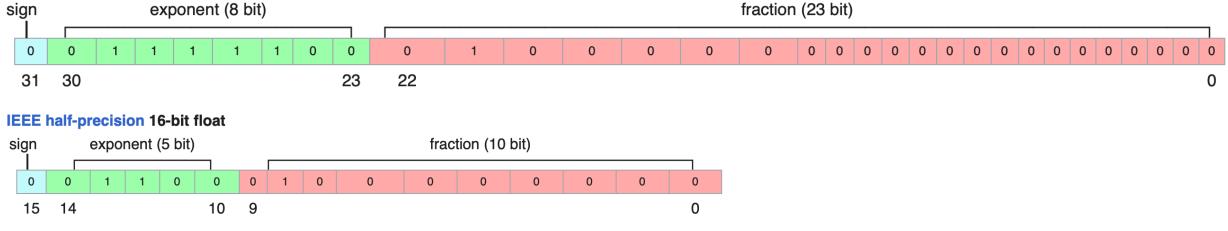
GPU/CPU bandpass ratio - a very small scaling issue (under investigation)



IEEE 754 single-precision 32-bit float exponent (8 bit) fraction (23 bit) sign



IEEE 754 single-precision 32-bit float





exponent (8 bit) fraction (23 bit) sign 0 0 **IEEE half-precision 16-bit float** fraction (10 bit) sign exponent (5 bit) 15 14

+ slightly faster
+ half the memory usage

IEEE 754 single-precision 32-bit float

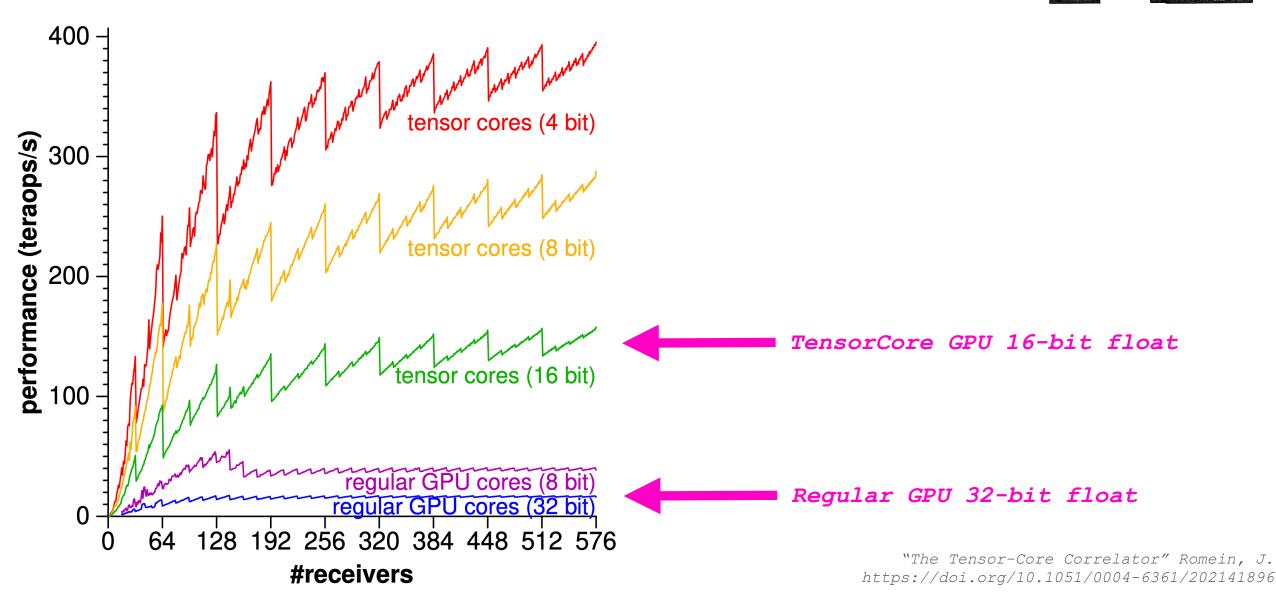
IEEE 754 single-precision 32-bit float



exponent (8 bit) fraction (23 bit) sign 0 0 **IEEE half-precision 16-bit float** fraction (10 bit) sign exponent (5 bit) 15 14

+ slightly faster
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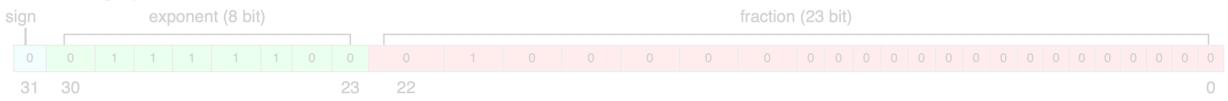
Hopper GPU



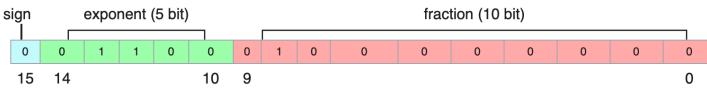
float16 ...



IEEE 754 single-precision 32-bit float



IEEE half-precision 16-bit float

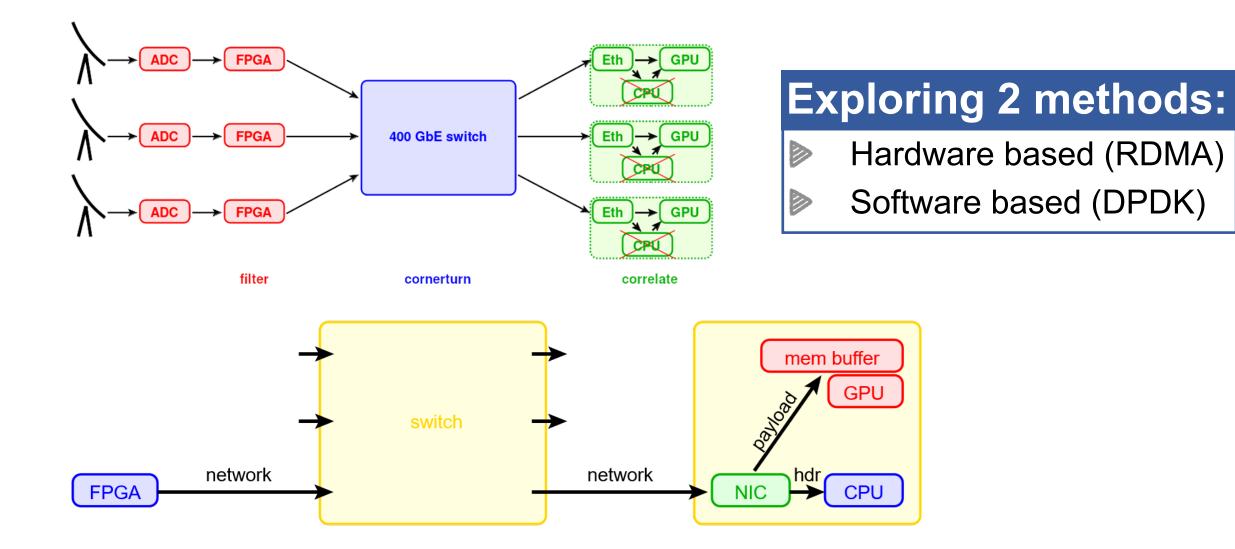


- + Grace CPU: slightly faster
- + both: half the memory usage
- + Hopper GPU: extremely fast
- sensitivity loss ~1%

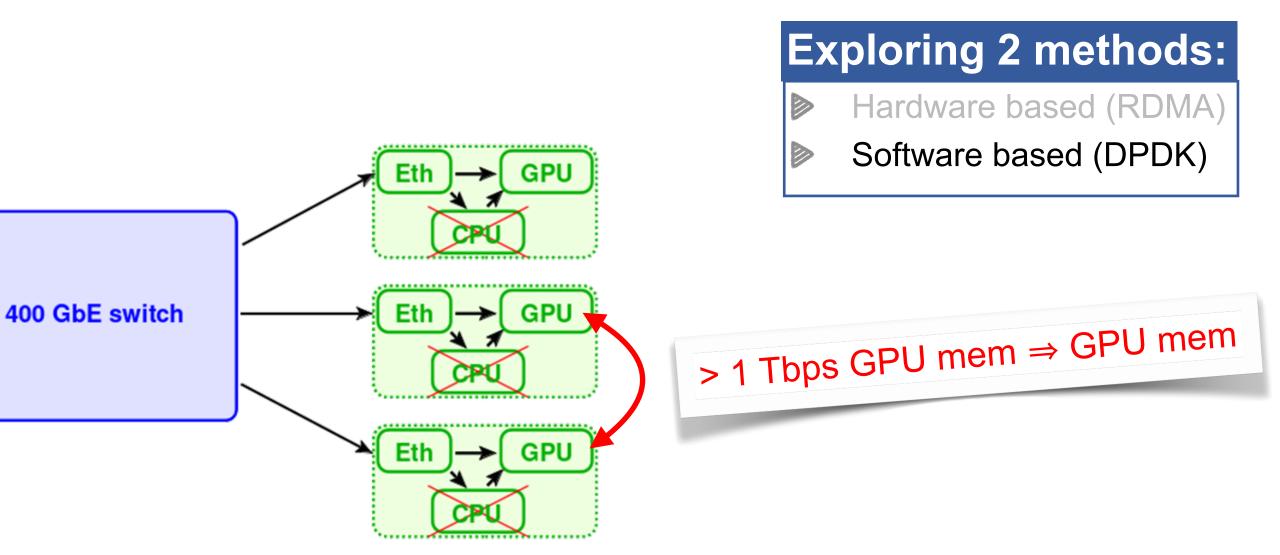
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High-speed data transport development



High-speed data transport development



BLOCKS

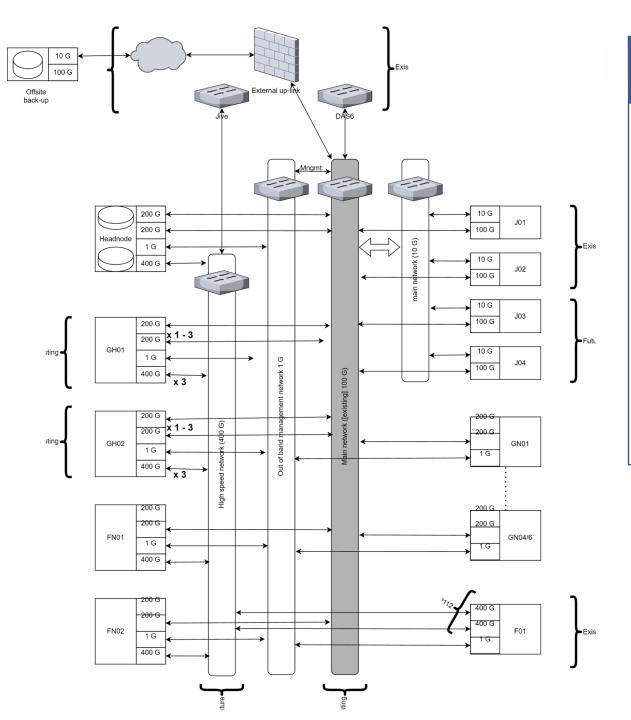


Longer term plans









New cluster:

• Systems: • Grace Hopper • Jetson Orin

• Architectures: • Various GPUs

AMD Epyc + ThreadRipper Pro CPU
 many cores

• Network:

- \circ 100-, 200-, 400 GbE
- \circ DMA from Storage or Networking into GPU



WP5 Data processing tool kit

"Provide modular, open-source and flexible analysis toolkit components for associated workflows to enable rapid, reproducible and scalable analysis of the large-volume and complex data products"

Rob Beswick (UMAN) & Marjolein Verkouter (JIV-ERIC)

15 institutes: JIV-ERIC, ASTRON, CSIC, MPG, ULEI, VENTSPILS, SDUUHEI, INAF, RU, EPFL, SKAO, CNIG, ICRAR, RATT, UP?





WP5 Tasks

- 1. Post processing pipelines: explore DASK
- 2. DASKy fringe fitter
- 3. Calibration and dynamic imaging
- 4. Bayesian imaging
- 5. Modular PAF back-end processing

"Modular open-source flexible components to process interferometry data"



Impact on VLBI

Grant number: 101093934



• High frequency VLBI

BLOCKS

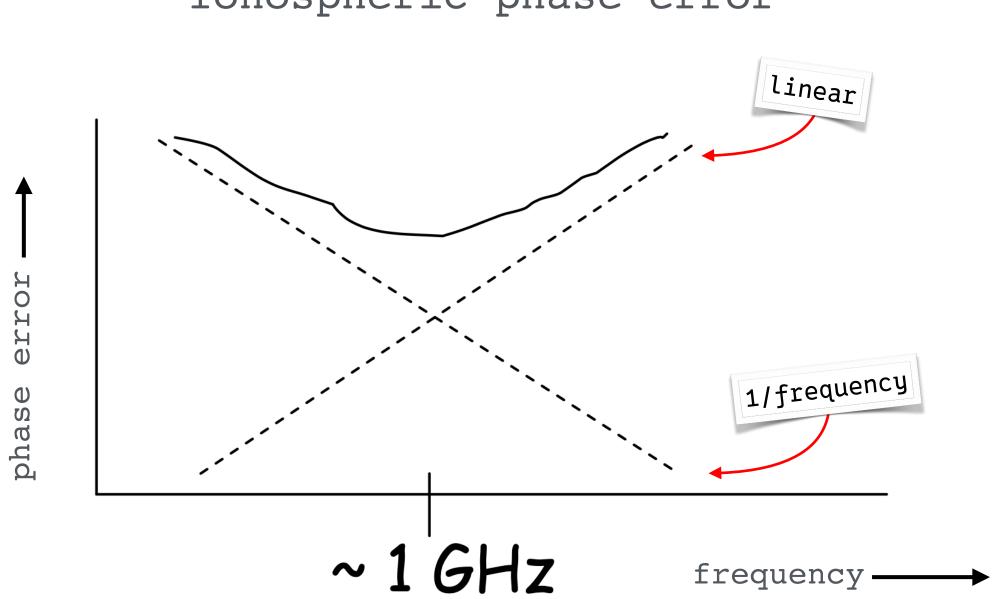
- Wide field VLBI data processing
- LOFAR2 with international stations
- Relevance for SKA-VLBI





5.1 & 5.2 DASK for automated workflows

- FAIR and scalable solutions for data processing
- Uniform approach for multiple facilities
- DASKy fringe fitter
- Discussion on data format

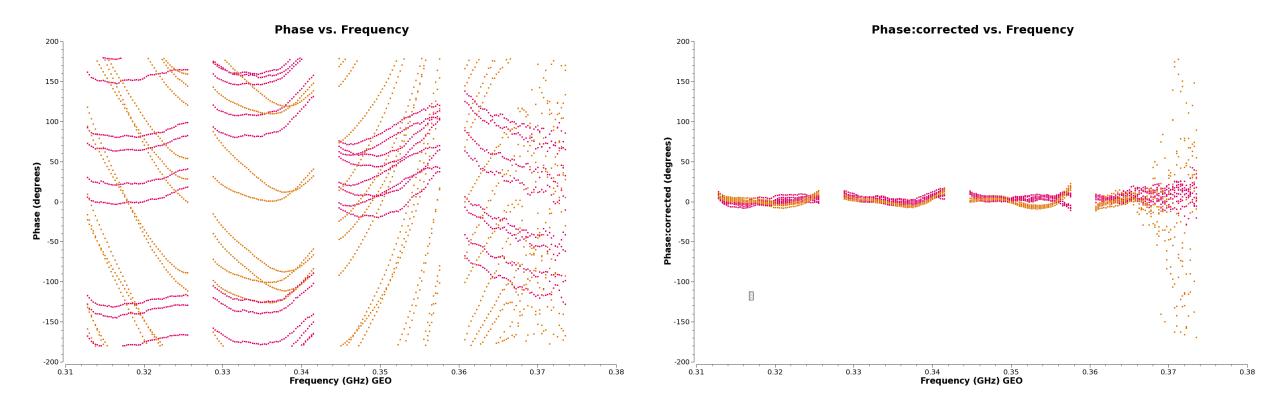


Ionospheric phase error

fringefit

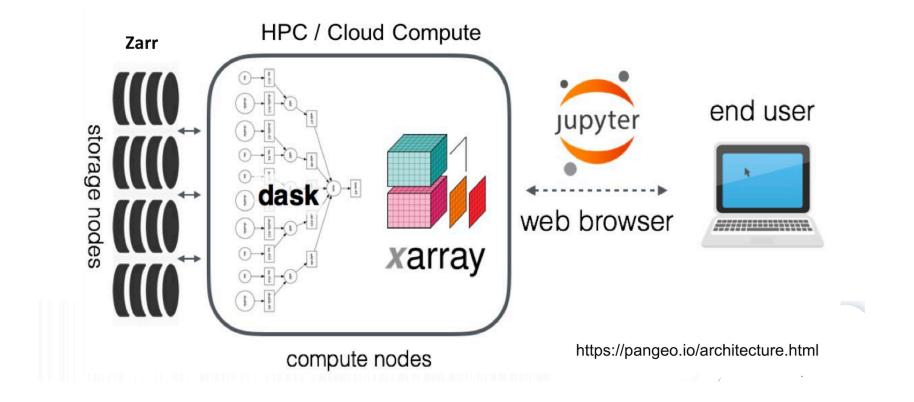
BEFORE

AFTER



fits linear + dispersive corrections in one go!

Porting fringefit algorithm to this framework already ongoing





5.3 Optimizing calibration

- VLBI data processing is perceived as DIFFICULT
- Relies on human experience: it is also biased
 - \Rightarrow automate calibration choices where possible

- Synthetic data generation
- Dynamic imaging of sparse datasets (EHT)

Use cases: EVN, SKA-VLBI



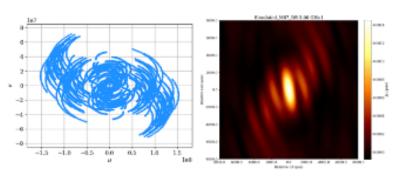
Synthetic data generation

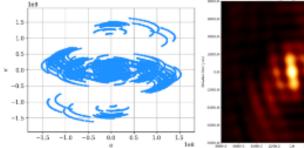
CASE 1: EVN (only NE antennas)

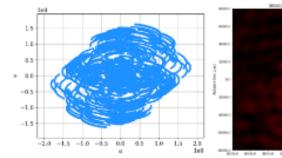
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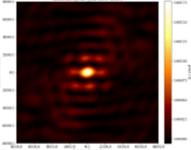
CASE 2: EVN + SKA core (4 km)

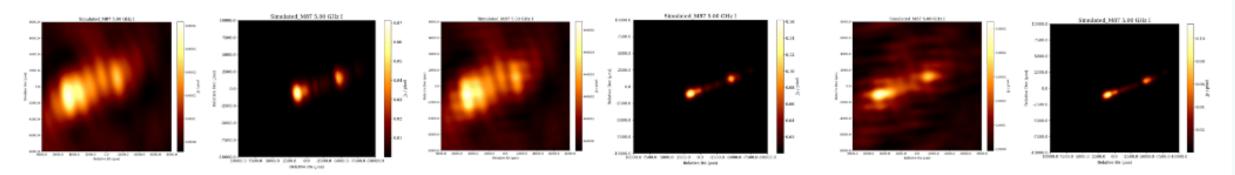
CASE 3: EVN + SKA + AVN











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Synthetic

data

generation

Software selection

Benchmark datasets

Astronomical datasets with realistic errors

Vary calibration settings

Compare results



WP 5.4 Bayesian inference imaging

- Exploit EHT experience for imaging sparse arrays
- Apply to centimeter wavelengths

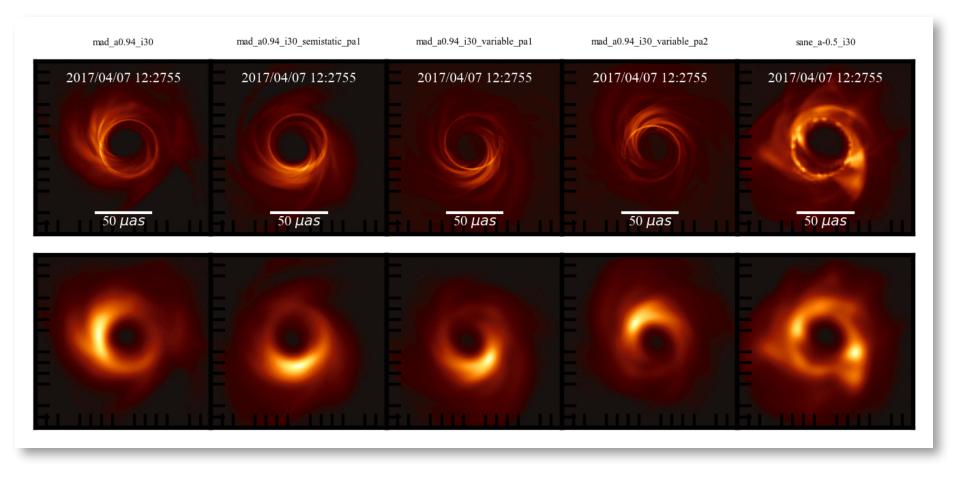
BLOCKS

• Links to synthetic data generation and calibration in WP5.3





Dynamic imaging



José L. Gomez (IAA)

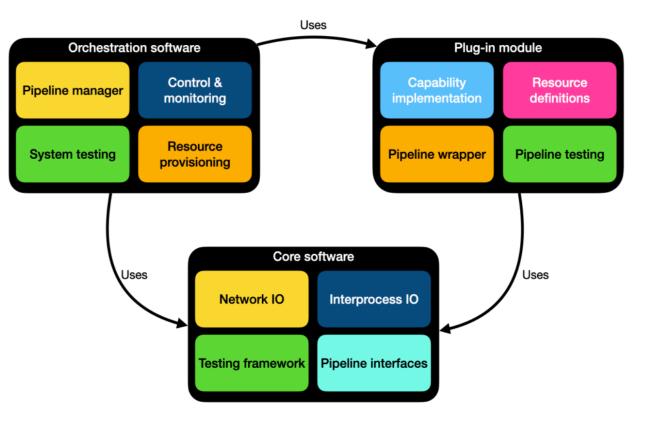


WP 5.5 PAF backend software

• Links to WP3

BLOCKS

- Plug-in architecture based on Effelsberg Direct Digitization software package
- PAF beamformer & correlator
- VLBI tests Ef, Mc, Yb & SKA-MPG



Building radio telescopes for the future: brick by brick

100